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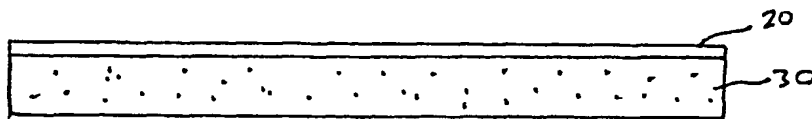
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(54) Title: A GLASS PRODUCT FOR USE IN ULTRA-THIN GLASS DISPLAY APPLICATIONS



(57) Abstract: The present invention is directed to a substrate product for use in the manufacture of active matrix liquid crystal display panels. The product includes a display substrate suitable for use as a display panel. The display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. The product also includes at least one support substrate removably attached to the display substrate.

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A GLASS PRODUCT FOR USE IN
ULTRA-THIN GLASS DISPLAY APPLICATIONS

RELATED APPLICATIONS

- [1] This application claims the priority of United States Application No. 10/613,972, filed July 3, 2003.

BACKGROUND OF THE INVENTION

1. Field of the Invention

- [2] The present invention relates generally to glass substrates, and particularly to a glass substrate product for use in AMLCD display manufacturing processes.

2. Technical Background

- [3] Liquid crystal displays (LCDs) are non-emissive displays that use external light sources. An LCD is a device that may be configured to modulate an incident polarized light beam emitted from the external source. LC material within the LCD modulates light by optically rotating the incident polarized light. The degree of rotation corresponds to the mechanical orientation of individual LC molecules within the LC material. The mechanical orientation of the LC material is readily controlled by the application of an external electric field. This phenomena is readily understood by considering a typical twisted nematic (TN) liquid crystal cell.

- [4] A typical TN liquid crystal cell includes two substrates and a layer of liquid crystal material disposed therebetween. Polarization films, oriented 90° one to the other, are disposed on the outer surfaces of the substrates. When the incident polarized

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light passes through the polarization film, it becomes linearly polarized in a first direction (e.g., horizontal, or vertical). With no electric field applied, the LC molecules form a 90° spiral. When incident linearly polarized light traverses the liquid crystal cell it is rotated 90° by the liquid crystal material and is polarized in a second direction (e.g., vertical, or horizontal). Because the polarization of the light was rotated by the spiral to match the polarization of the second film, the second polarization film allows the light to pass through. When an electric field is applied across the liquid crystal layer, the alignment of the LC molecules is disrupted and incident polarized light is not rotated. Accordingly, the light is blocked by the second polarization film. The above described liquid crystal cell functions as a light valve. The valve is controlled by the application of an electric field. Those of ordinary skill in the art will also understand that, depending on the nature of the applied electric field, the LC cell may also be operated as a variable light attenuator.

[5] An Active Matrix LCD (AMLCD) typically includes several million of the aforementioned LC cells in a matrix. Referring back to the construction of an AMLCD, one of the substrates includes a color filter plate and the opposing substrate is known as the active plate. The active plate includes the active thin film transistors (TFTs) that are used to control the application of the electric field for each cell or subpixel. The thin-film transistors are manufactured using typical semiconductor type processes such as sputtering, CVD, photolithography, and etching. The color filter plate includes a series of red, blue, and green organic dyes disposed thereon corresponding precisely with the subpixel electrode area of the opposing active plate. Thus, each sub-pixel on the color plate is aligned with a transistor controlled electrode disposed on the active plate, since each sub-pixel must be individually controllable. One way of addressing and controlling each sub pixel is by disposing a thin film transistor at each sub pixel.

[6] The properties of the aforementioned substrate glass are extremely important. The physical dimensions of the glass substrates used in the production of AMLCD devices must be tightly controlled. The fusion process, described in U.S. Pat. Nos. 3,338,696 (Dockerty) and 3,682,609 (Dockerty), is one of the few processes capable of delivering substrate glass without requiring costly post substrate forming finishing operations, such as lapping, grinding, and polishing. Further, because the active plate is manufactured using the aforementioned semiconductor type processes, the substrate

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must be both thermally and chemically stable. Thermal stability, also known as thermal compaction or shrinkage, is dependent upon both the inherent viscous nature of a particular glass composition (as indicated by its strain point) and the thermal history of the glass sheet, which is a function of the manufacturing process. Chemical stability implies a resistance to the various etchant solutions used in the TFT manufacturing process.

[7] Currently, there is a demand for larger and larger display sizes. This demand, and the benefits derived from economies of scale, are driving AMLCD manufacturers to process larger sized substrates. However, this raises several issues. First, the increased weight of the larger display is problematic. While consumers want larger displays, there is also a demand for lighter and thinner displays. Unfortunately, if the thickness of the glass is decreased, the elastic sag of the glass substrate becomes a problem. The sag is further exacerbated when the size of the substrate is increased to make larger displays. Presently, it is difficult for TFT manufacturing technology to accommodate fusion glass thinner than 0.5mm because of glass sag. Thinner, larger substrates have a negative impact on the processing robotics' ability to load, retrieve, and space the glass in the cassettes used to transport the glass between processing stations. Thin glass can, under certain conditions, be more susceptible to damage, leading to increased breakage during processing.

[8] In one approach that has been considered, a thick display glass substrate is employed during TFT processing. After the active layer is disposed on the glass substrate, the opposite face of the glass substrate is thinned by grinding and/or polishing. One drawback to this approach is that it requires an additional grinding/polishing step. The expense of the additional step(s) is thought to be quite high.

[9] Therefore, it would be highly desirable to provide an ultra-thin fusion glass substrate that would allow for the direct formation of thin-film transistors without having to subject the display substrate to an additional polishing and/or grinding step. Current glass substrate thicknesses are on the order of 0.6 – 0.7mm. By decreasing the thickness of the substrate to 0.3mm, a 50% reduction in weight will be achieved. However, ultra-thin glass has an unacceptably high degree of sag and can be prone to breakage. What is needed is an ultra-thin glass substrate product that may be employed

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in the state-of-the art TFT manufacturing processes without the aforementioned problems.

SUMMARY OF THE INVENTION

[10] The present invention addresses the above-described needs. The present invention provides an ultra-thin fusion glass substrate that can be used in conventional TFT manufacturing processes. The glass substrate product of the present invention has a smoothness that allows the direct formation of thin-film transistors without having to perform a polishing or grinding step. The present invention provides ultra-thin glass substrates having a thickness in the range between 0.4mm and 0.1mm. One aspect of the present invention is a substrate product for use in the manufacture of active matrix liquid crystal display panels. The product includes a display substrate suitable for use as a display panel. The display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. The product also includes at least one support substrate removably attached to the display substrate.

[11] In another aspect, the present invention includes a method for making a substrate product for use in the manufacture of active matrix liquid crystal display panels. The method includes forming a display substrate suitable for use as a display panel. The display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. At least one support substrate is attached to the display substrate.

[12] In another aspect, the present invention includes a method for making an active matrix liquid crystal display panel. The method includes forming a plurality of display substrates suitable for use as display panels. Each display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. A support substrate is attached to each display substrate. An active matrix liquid crystal display panel is produced using a

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first display substrate and a second display substrate. Subsequently, the support substrates attached to each of the display substrates are removed.

[13] In another aspect, the present invention includes an active matrix liquid crystal display panel that includes a first display substrate. The first display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. The panel also includes a second display substrate. The second display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. A liquid crystal material is disposed between the first display substrate and the second display substrate.

[14] Additional features and advantages of the invention will be set forth in the detailed description which follows, and in part will be readily apparent to those skilled in the art from that description or recognized by practicing the invention as described herein, including the detailed description which follows, the claims, as well as the appended drawings.

[15] It is to be understood that both the foregoing general description and the following detailed description are merely exemplary of the invention, and are intended to provide an overview or framework for understanding the nature and character of the invention as it is claimed. The accompanying drawings are included to provide a further understanding of the invention, and are incorporated in and constitute a part of this specification. The drawings illustrate various embodiments of the invention, and together with the description serve to explain the principles and operation of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

[16] Figure 1 is a diagrammatic depiction of the substrate product of the present invention in accordance with a first embodiment of the present invention;

[17] Figure 2 is a diagrammatic depiction of the substrate product of the present invention in accordance with a second embodiment of the present invention;

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[18] Figure 3 is a diagrammatic depiction of the substrate product of the present invention in accordance with a third embodiment of the present invention;

[19] Figure 4 is a diagrammatic depiction of the substrate product of the present invention in accordance with a fourth embodiment of the present invention;

[20] Figure 5 is a diagrammatic depiction of an alternate embodiment of the substrate product depicted in Figure 1;

[21] Figure 6 is a detail view showing the disposition of a TFT transistor on the display substrate depicted in Figure 1; and

[22] Figure 7A-7B are detail views illustrating TFT processing in accordance with the present invention.

DETAILED DESCRIPTION

[23] Reference will now be made in detail to the present exemplary embodiments of the invention, examples of which are illustrated in the accompanying drawings.

Wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts. An exemplary embodiment of the substrate product of the present invention is shown in Figure 1, and is designated generally throughout by reference numeral 10.

[24] In accordance with the invention, the present invention is directed to a substrate product for use in the manufacture of active matrix liquid crystal display panels. The product includes a display substrate suitable for use as a display panel. The display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. The product also includes at least one support substrate removably attached to the display substrate. Accordingly, the present invention provides an ultra-thin fusion glass substrate that can be used in state-of-the art TFT manufacturing processes. The display substrate has a smoothness that allows the direct formation of thin-film transistors without having to perform a polishing or grinding step.

[25] As embodied herein, and depicted in Figure 1, a diagrammatic depiction of the substrate product 10 of the present invention in accordance with a first embodiment of the present invention is disclosed. Substrate product 10 is a glass-on-glass laminate

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that has an overall thickness in the range between 0.6 - 0.7mm. Those skilled in the art will understand that this range is compatible with conventional TFT processing techniques. Product 10 includes display substrate 20 and support substrate 30. Display substrate 20 has a thickness in the range between 0.1mm and 0.4mm. The thickness of support substrate 30 depends on the thickness of the display substrate and the overall thickness of product 10.

[26] Display substrate 20 may be of any substrate type suitable for use in a LCD display panel, as long as the display substrate has a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding. Reference is made to U.S. Patent No. 5,374,595 and U.S. Patent No. 6,060,168, which are incorporated herein by reference as though fully set forth in their entirety, for a more detailed description of the composition of the glass comprising display substrate 20.

[27] It will be apparent to those of ordinary skill in the pertinent art that modifications and variations can be made to support substrate 30 of the present invention depending on the means used to separate support layer 30 from display substrate 20 after TFT processing is completed. For example, support substrate 30 may be comprised of a sacrificial non-display glass composition (lost glass) suitable for chemical dissolution without subsequent damage to the display substrate. In another embodiment, support substrate 30 may be comprised of a relatively soft non-display glass composition removable by grinding/polishing without subsequent damage to the display substrate. Those of ordinary skill in the art will recognize that many varieties of relatively inexpensive glasses may be used in the production of support layer 30.

[28] A laminate substrate product 10, having surfaces which are essentially defect-free and equivalent in smoothness to polished surfaces, can be fashioned in accordance with the following steps. First, two alkali metal-free batches of different compositions are melted. The batch for the display glass must exhibit a strain point higher than 600° C, and be relatively insoluble in an acid solution. The batch for the support glass substrate consists, expressed in terms of cation percent on the oxide basis, of

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SiO ₂	27-47	B ₂ O ₃	0-40	SrO and/or BaO	0-10
Al ₂ O ₃	15-43	MgO	0-4	ZnO	0-7
CaO	5-25			MgO + SrO + BaO + ZnO	0-15

[29] One current candidate for the support glass substrate consists, expressed in terms of cation percent on the oxide basis, of SiO₂ 41, Al₂O₃ 18, B₂O₃ 32 and CaO 9.

[30] Reference is made to U.S. Patent No. 4,102,664 and U.S. Patent No. 5,342,426, which are incorporated herein by reference as though fully set forth in their entirety, for a more detailed description of a method for making laminated bodies.

[31] The support glass is at least 1000 times more soluble in the same acid solution and exhibits a linear coefficient of thermal expansion from its setting point to room temperature within about $5 \times 10^{-7} / ^\circ \text{C}$ of that of the display glass substrate. The support glass also exhibits a strain point higher than 600° C and relatively close to the strain point of the display glass substrate. The support glass is characterized by a linear coefficient of thermal expansion over the temperature range of 0° C - 300° C between $20-60 \times 10^{-7} / ^\circ \text{C}$.

[32] The molten batches are brought together simultaneously while in the fluid state to form a laminated sheet wherein the display glass is essentially completely enclosed within the support glass. The layers are fused together at a temperature where the melts are in fluid form to provide an interface therebetween which is defect-free. The laminated sheet is cooled to solidify each glass present in fluid form.

[33] As discussed above, after TFT processing is completed, an acid solution is used to dissolve the support glass. The resultant surface of the display glass, from which the support glass has been removed, is rendered essentially defect-free and is equivalent in smoothness to a polished glass surface. The dissolution of the soluble glass (lost glass) in an acid bath will be carried out after the laminated sheet has arrived at its destination. Thus, sheets cut from the laminate can be readily stacked and shipped to the LCD display device manufacturer.

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[34] The liquidus temperature values of the two glasses will preferably be below the temperature at which lamination is conducted in order to prevent the occurrence of devitrification during the select forming process.

[35] Finally, in accordance with conventional practice, the laminated sheet may be annealed to avoid any detrimental strains, most preferably during the cooling step, although the cooled laminate may be reheated and thereafter annealed. As has been explained above, the strain points of the present inventive glasses are sufficiently high that annealing may not be required in the formation of a-Si devices.

[36] As embodied herein, and depicted in Figure 2, an alternate embodiment of substrate product 10 of the present invention is disclosed. Again, substrate product 10 has an overall thickness of between 0.6-0.7mm, which is compatible with current TFT processing techniques. Display substrate 20 has a thickness in the range between 0.1mm and 0.4mm. The thickness of support substrate 30 depends on the thickness of the display substrate and the overall thickness of product 10. In this embodiment, support substrate 30 is tacked onto display substrate 20 using adhesive 40. Adhesive 40 is a high temperature flux that is formulated to withstand high temperatures of poly-Si processing, which may approach 450° C. Further, support substrate 30 and adhesive 40 are of a type to withstand the chemical, mechanical, and optical environmental stresses encountered during TFT processing. Reference is made to U.S. Patent 5,281,560 which is incorporated herein by reference as though fully set forth in its entirety, for a more detailed description of possible adhesives.

[37] The composition of display substrate 20 and support substrate 30 were disclosed above in the discussion of the first embodiment. Both display substrate 20 and support substrate 30 may be fabricated using fusion draw processes. Reference is made to U.S. Patent No. 3,338,696 and U.S. Patent 3,682,609, which are incorporated herein by reference as though fully set forth in their entirety, for a more detailed explanation of a system and method for producing glass substrates using the fusion draw technique. By using higher gear ratio drives and composite pulling rolls, the fusion draw technique is well able to produce glass substrates having a thickness of approximately 100 microns (0.1mm). One advantage of using a fusion glass as a support substrate is its superior flatness. The flatness of the surface is important because it minimizes focusing errors during the photolithographic steps performed during TFT processing. Further the linear

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coefficient of thermal expansion (CTE) of support substrate 30 can be made to match that of the display glass. If the substrates have dissimilar CTEs, product warping may occur. Another advantage of using the fusion draw process is the ability to make a support substrate having a higher modulus of elasticity.

[38] The above described second embodiment has the same advantages as the first embodiment. Substrate product 10 has an overall thickness, weight, and sag characteristics that are compatible with state-of-the art TFT processing. The use of sacrificial support layer 30 enables the fabrication of lighter and thinner display panels.

[39] Referring to Figure 3, another alternate embodiment of the present invention is disclosed. In this embodiment, support substrate 30 is a fusion glass sheet having holes 32 drilled through the glass perpendicular to the surface of the substrate. The size and number of holes depends on the release mechanism used to separate product 10 from the processing station. In one embodiment, the release mechanism employs lifting pins made from a soft non-abrasive material such as Teflon. In another embodiment, the release mechanism applies gas or liquid to lift the substrate. The physical configuration of support substrate 30 may also include corrugation or "egg crate" designs. Support substrate 30 may also be comprised of recyclable glass. After processing, substrate 30 may be ground into cullet and reformed using one of the above described fabrication techniques. Substrate 30 may also be re-used without being ground into cullet.

[40] In another embodiment, support substrate 30 includes a lip that surrounds display substrate 20. In this embodiment, a vacuum may be applied to the display substrate 20 via holes 32 to keep product 10 in place during processing. In this embodiment, adhesive 40 may not be necessary. However, if no adhesive is applied, a diamond like coating (DLC) is applied to the surface of support substrate 30 on which display substrate 20 rests. The DLC aids in the distribution of heat, is scratch resistant, and allows the display substrate 20 to be easily released after processing. In this embodiment, a gas or liquid may be applied to release display substrate 20.

[41] As embodied herein, and depicted in Figure 4, yet another embodiment of the present invention is disclosed. Substrate 10 includes display substrate 20 coated on both sides with lost glass substrates 300 and 302. This embodiment provides additional protection to display substrate 20. Prior to TFT processing and disposition, one of the support layers is removed. After TFT processing, the second layer is removed and the

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plastic polarization film is applied to the backside of display substrate 20. As described above, the properties of the lost glass would have to be compatible with TFT processing conditions.

[42] Referring to Figure 5, yet another alternate embodiment of substrate product 10 is disclosed. This embodiment is similar to the embodiment shown in Figure 1, in that substrate product 10 is a laminate that includes display substrate 20 and support substrate 30. However, product 10 may be shipped to the LCD manufacturer having a pre-processing layer 310 disposed thereon. Layer 310 includes a silica layer 312 disposed on display substrate 20. A silicon layer 314 is disposed on silica layer 312. Both layers may be formed using chemical vapor deposition (CVD) techniques. The advantage of this embodiment will be apparent after the following discussion.

[43] Referring to Figure 6, a cross-sectional view of a TFT on an active substrate is shown. Active substrate 100 of the present invention includes display substrate 20 disposed on support substrate 30. Using the reference number convention employed in Figure 5, insulating silica layer 312 is disposed on display substrate 20. Active layer 314, formed from a semiconductor (Si) film, is disposed on insulating layer 312. A gate insulation layer is disposed on active layer 314. Gate 400 is disposed on gate insulator 320 over the center of the active area. Source 316 and drain 318 are formed in the active area. During operation, current flows from the source 316 to the drain 318 when power is applied to the transistor. Pixel actuation is controlled by a circuit coupled to drain 318. The configuration of the TFT transistor 100 shown in Figure 6 is for illustration purposes, and the present invention should not be construed as being limited to a transistor of this type. Accordingly, Figure 6 illustrates the use of a sacrificial support layer 30 to enable the fabrication of TFTs on lighter and thinner display substrates having a thickness between 0.1 – 0.4mm. Those skilled in the art will understand that substrate product 10 has an overall thickness, weight, and sag characteristics that are compatible with conventional TFT processing. Thus, the present invention may be employed without any significant alteration to TFT manufacturing processes. Once TFT processing is complete, the sacrificial layer may be removed using one of the above described techniques.

[44] Figure 7A and Figure 7B are detail views illustrating a method for making an active matrix liquid crystal display panel in accordance with the present invention. As

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shown in Figure 7A, an active matrix liquid crystal display panel is produced using substrate product 10 and substrate product 12, both fabricated in accordance with the principles of the present invention. A plurality of thin film transistors are disposed on display substrate 200 of substrate product 10 to produce an active substrate. A color filter is disposed on display substrate 202 on product 12 to produce a color filter substrate. Subsequently, liquid crystal material 50 is placed between active substrate 200 and color filter substrate 202, and sealed with an appropriate material. As shown in Figure 7B, the support substrates 30 attached to each of the display substrates (200, 202) are removed. To illustrate the advantages of the present invention, it is noted that if display substrate 200 and display 202 each have a thickness of 0.3mm, the resultant display panel 700 will be 50% lighter than conventional AMLCD panels, since the thicknesses of conventional display substrates are on the order of 0.6 – 0.7mm. If display substrate 200 and display substrate 202 each have a thickness of 0.1mm, the resultant display panel 700 will be approximately 80% lighter than conventional AMLCD panels.

[45] It will be apparent to those skilled in the art that various modifications and variations can be made to the present invention without departing from the spirit and scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

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What is claimed is:

1. A substrate product for use in the manufacture of active matrix liquid crystal display panels, the product comprising:

a display substrate suitable for use as a display panel, the display substrate having a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding; and
at least one support substrate removably attached to the display substrate.

2. The product of claim 1, wherein the thickness of the display substrate is in a range between 0.1mm and 0.4mm.

3. The product of claim 1, wherein the product has an overall thickness that is less than or equal to 0.7mm.

4. The product of claim 1, wherein the product is a glass-on-glass laminate, the at least one support substrate comprising a sacrificial non-display glass composition suitable for chemical dissolution without subsequent damage to the display substrate.

5. The product of claim 1, wherein the product is a glass-on-glass laminate, the at least one support substrate comprising a relatively soft non-display glass composition removable by grinding/polishing without subsequent damage to the display substrate.

6. The product of claim 1, wherein the at least one support substrate is a recyclable glass substrate attached to the display substrate by means of an adhesive.

7. The product of claim 6, wherein the adhesive and the recyclable glass substrate are of a type to withstand thermal, chemical, mechanical, and optical environmental stresses encountered during TFT processing steps.

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8. The product of claim 6, wherein the display substrate and the recyclable glass substrate are fusion glass substrates.

9. The product of claim 8, wherein the at least one support substrate is characterized by a large modulus of elasticity relative to the display substrate.

10. The product of claim 8, wherein the recyclable glass substrate includes a diamond like coating (DLC), the DLC being interposed between the recyclable glass substrate and the display substrate.

11. The product of claim 1, wherein the at least one support substrate is characterized by a low density relative to the display substrate.

12. The product of claim 1, wherein the at least one support substrate includes a corrugated surface.

13. The product of claim 1, wherein the at least one support substrate includes a surface having an egg-carton pattern disposed thereon.

14. The product of claim 1, wherein the at least one support substrate includes a plurality of holes.

15. The product of claim 1, wherein the at least one support substrate includes a first support substrate disposed on a first side of the display substrate and a second support substrate disposed on a second side of the display substrate.

16. The product of claim 15, wherein the first support substrate and the second support substrate are comprised of a relatively soft non-display glass composition removable by grinding/polishing without subsequent damage to the display substrate.

17. The product of claim 15, wherein the first support substrate is comprised of a relatively soft non-display glass composition removable by grinding/polishing without

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subsequent damage to the display substrate, and the second substrate includes a layer of silicon.

18. The product of claim 17, wherein the second substrate includes a layer of SiO_2 disposed on the display substrate and a layer of silicon disposed on the layer of SiO_2 .

19. A method for making a substrate product for use in the manufacture of active matrix liquid crystal display panels, the method comprising:

forming a display substrate suitable for use as a display panel, the display substrate having a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding; and
attaching at least one support substrate to the display substrate.

20. The method of claim 19, wherein the step of forming includes melting a first glass composition to form a first molten glass material.

21. The method of claim 20, wherein the step of attaching further comprises:

melting at least one second glass composition to form at least one second molten glass material;
combining the first molten glass material with the at least one second molten glass material while both the first molten glass material and the at least one second molten glass material are in a liquid state to thereby produce a display substrate layer and at least one support substrate layer;
fusing the display substrate layer and the at least one support substrate layer at a temperature at which both the display substrate layer and the at least one support substrate layer are sufficiently fluid to provide a defect free interface therebetween; and
cooling the fused display substrate layer and the at least one support substrate layer to thereby form a glass-on-glass laminate product.

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22. The method of claim 21, wherein the at least one support substrate layer is comprised of a sacrificial non-display glass composition suitable for chemical dissolution without subsequent damage to the display substrate layer.

23. The method of claim 21, wherein the at least one support substrate is comprised of a relatively soft non-display glass composition removable by grinding/polishing without subsequent damage to the display substrate.

24. The method of claim 19, wherein the at least one support substrate includes a first support substrate disposed on a first side of the display substrate and a second support substrate disposed on a second side of the display substrate.

25. The method of claim 24, wherein the first support substrate and the second support substrate are comprised of a relatively soft non-display glass composition removable by grinding/polishing without subsequent damage to the display substrate.

26. The method of claim 19, wherein the step of attaching further comprises:
 applying an adhesive to the display substrate;
 joining the at least one support substrate to the display substrate with the
 adhesive interposed therebetween; and
 curing the adhesive.

27. The method of claim 26, wherein the adhesive and the at least one support substrate are of a type to withstand thermal, chemical, mechanical, and optical environmental stresses encountered during TFT processing steps.

28. The method of claim 26, further comprising the step of disposing a diamond like coating (DLC) on the at least one support substrate prior to the step of applying an adhesive.

29. A method for making an active matrix liquid crystal display panel, the method comprising:

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forming a plurality of display substrates suitable for use as display panels, each display substrate having a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding;

attaching a support substrate to each display substrate;

producing an active matrix liquid crystal display panel with a first display substrate of the plurality of display substrates and a second display substrate of the plurality of display substrates; and

removing the support substrate attached to the first display substrate and the second display substrate.

30. The method of claim 29, wherein the step of producing further comprises the steps of:

disposing a plurality of thin film transistors on the first display substrate;

disposing the color filter on a second display substrate, the color filter including a red sub-pixel, a green sub-pixel, and a blue sub-pixel for each thin-film transistor disposed on the first display substrate;

placing liquid crystal material between the first display substrate and the second display substrate; and

sealing the first display substrate and the second display substrate.

31. The method of claim 29, further comprising the step of applying a polarizing filter to the active matrix liquid crystal display panel.

32. The method of claim 29, wherein the step of attaching further comprises:

melting a first glass composition to form a first molten glass material;

melting a second glass composition to form a second molten glass material;

combining the first molten glass material with the second molten glass material while both the first molten glass material and the second molten glass material are in a liquid state to thereby produce a display substrate layer and a support substrate layer;

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fusing the display substrate layer and the support substrate layer at a temperature at which both the display substrate layer and the support substrate layer are sufficiently fluid to provide a defect free interface therebetween; and cooling the fused display substrate layer and the support substrate layer to thereby form a glass-on-glass laminate product.

33. The method of claim 32, wherein the step of removing the support substrate includes the step of chemically dissolving the support substrate without subsequent damage to the glass display substrate.

34. The method of claim 32, wherein the step of removing the support substrate includes the step of grinding and/or polishing the support substrate without subsequent damage to the glass display substrate.

35. The method of claim 29, wherein the step of attaching further comprises:
applying an adhesive to the display substrate;
joining the support substrate to the display substrate with the adhesive disposed therebetween; and
curing the adhesive.

36. The method of claim 35, wherein the step of removing the support substrate includes the step of chemically dissolving the adhesive without subsequent damage to the glass display substrate.

37. The method of claim 35, wherein the step of removing the support substrate includes the step of applying a mechanical force to destroy the adhesive bond between the display substrate and the support substrate.

38. An active matrix liquid crystal display panel comprising:
a first display substrate, the first display substrate having a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film

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transistors thereon without a prior processing step of polishing and/or grinding;

a second display substrate, the second display substrate having a thickness less than or equal to 0.4mm, a composition that is substantially alkali free, and a surface smoothness that allows the direct formation of thin-film transistors thereon without a prior processing step of polishing and/or grinding; and

liquid crystal material disposed between the first display substrate and the second display substrate.

39. The panel of claim 38, wherein the first display substrate includes thin-film transistors disposed thereon, and the second display substrate includes a color filter disposed thereon, the color filter including a red sub-pixel, a green sub-pixel, and a blue sub-pixel for each thin-film transistor disposed on the first display substrate.

40. The panel of claim 38, wherein the thickness of the first display substrate is substantially within a range between 0.4mm and 0.1mm.

41. The panel of claim 38, wherein the thickness of the second display substrate is substantially within a range between 0.4mm and 0.1mm.

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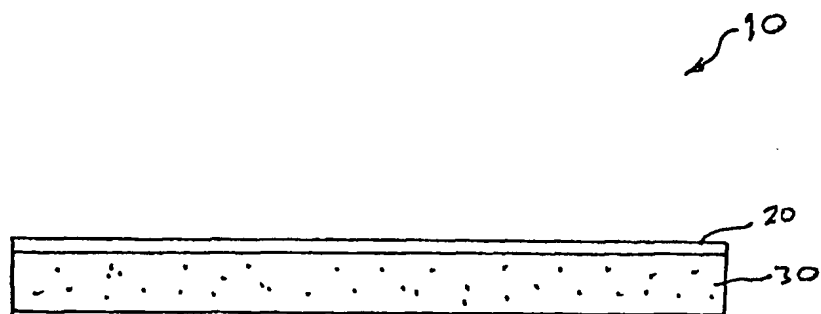


FIGURE 1

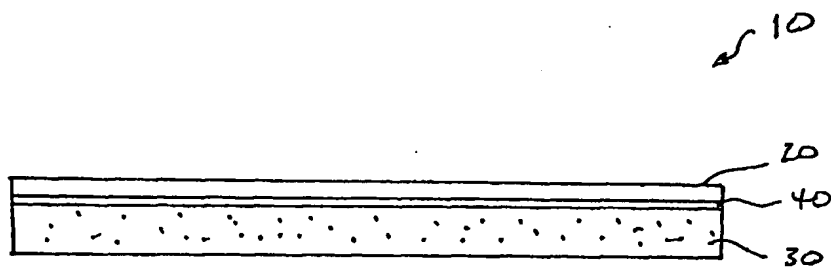
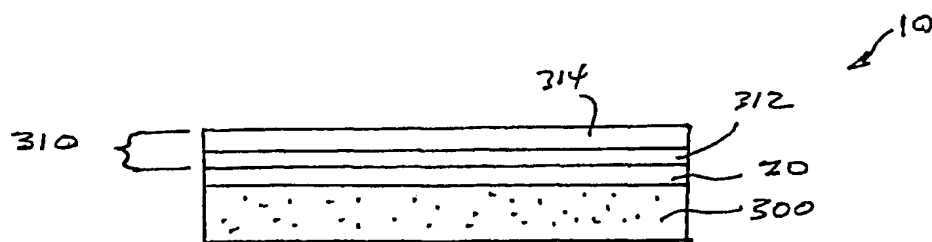
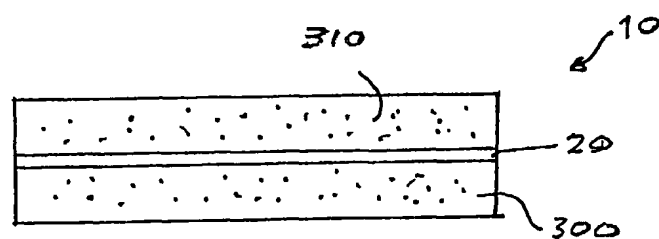
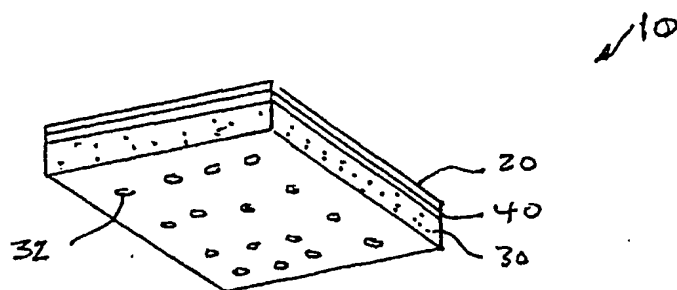


FIGURE 2

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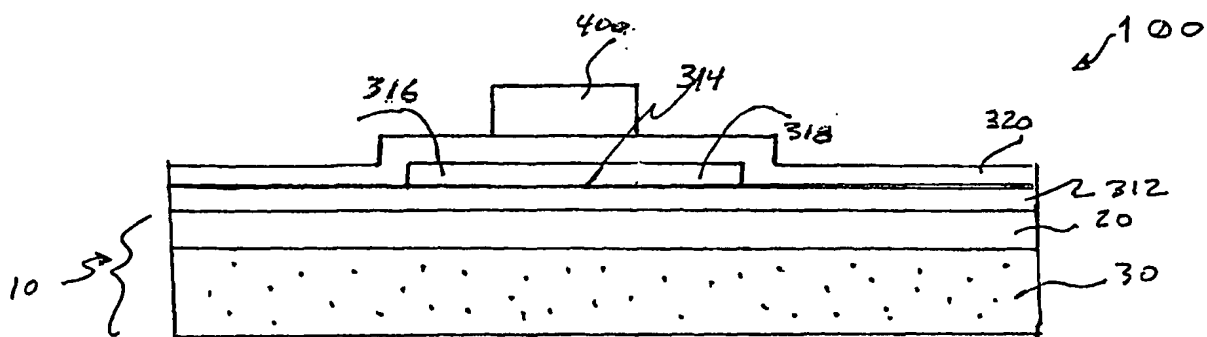


FIGURE 6

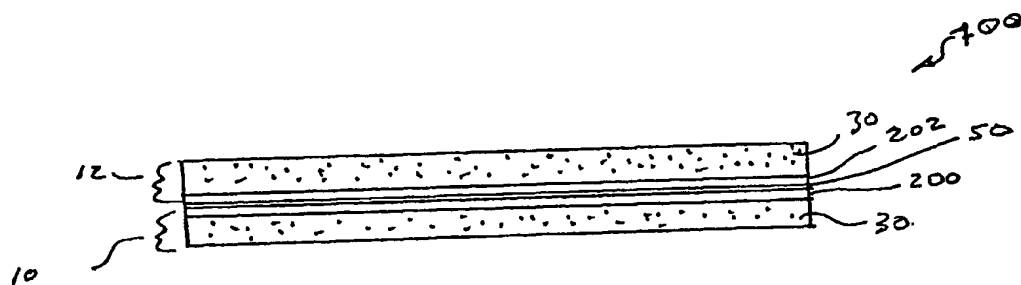


FIGURE 7A

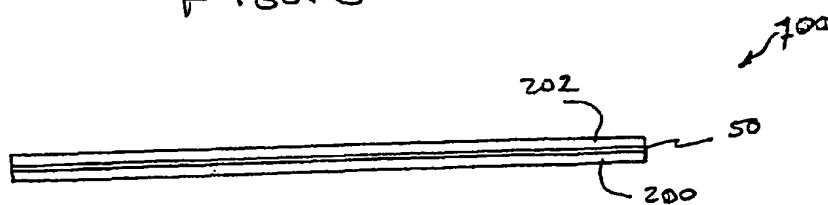


FIGURE 7B